

Description

CPU

- 5 ns instruction cycle time @ 200 MHz clock
- Register based design
- RTOS and HLL instructions
- 16 Mbyte linear address space
- 128 interrupts (inc. 2 fast interrupts)

Single cycle MAC

- 16x16-Bit multiplier
- 32-Bit barrel shifter
- 32/16-Bit divider

Multiple high bandwidth data bus systems

- 16-Bit high performance system bus (Ext. bus 8/16 Bit, muxed/demuxed, 8 configurable chip selects; X-Bus support)
- 16-Bit enhanced peripheral bus (PDBUS+)
- 64-Bit internal program memory bus (single cycle access)
- 16-Bit data memory bus (0 cycle access)

Debug

- JTAG
- OCDS Level 1
- Bondout chip available

Technical Data

- Synthesizable Core
- Silicon proven in 0.18 μm technology
- Supply voltage 1.8 V
- 0.9 mW/MHz with
 - CPU, PMU, DMU, EBC, IAI (32 nodes)
 - 512 Kbyte Code-SRAM, 8 Kbyte Data-SRAM, 2 Kbyte DPRAM
 - Port 2, 3, 5
 - Peripherals: SSC, ASC, GPT12, PWM
 - System control unit (SCU)
 - JTAG

For further information on technology, delivery terms and conditions and prices please see our web-site for contact details. The information herein is given to describe certain components and shall not be considered as warranted characteristics. Terms of delivery and rights to technical change reserved. We hereby disclaim any and all warranties, including but not limited to warranties of non-infringement, regarding circuits, descriptions and charts stated herein. Infineon Technologies is an approved CECC manufacturer.

Warnings

Due to technical requirements components may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies Office.

Infineon Technologies Components may only be used in life-support devices or systems with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system, or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body, or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.

©Infineon Technologies AG 2001. All Rights Reserved. Ordering # B158-H7813-x-x-7600. Printed in Germany. PS 02013. NB

Infineon Technologies AG
St.-Martin-Strasse 53
D-81541 München

Infineon Technologies
Corporation
1730 North First Street
San Jose, CA 95112 USA

www.infineon.com