XC16x Emulation Concept

- **OCDS** Level1: On-Chip-Debug-Support

  - Entry level debug system

  - Low cost concept

  - Available on all production chip

  - Controlled via a JTAG link (8 wires)

- OCE: On Chip Emulation

  - High end level debug system

  - Based on an
    Emulation Device (Production Chip + Carrier)

XC166 architecture