C166S-V2 CPU Architecture External Bus Controller (EBC) Additional features to C16x

XC16x

- EBC (new Register set)

    – EBCMOD0 controls alternate pin functions

    – EBCMOD1 controls the general use of port pins

    – TCONCSx controls the cycle times of the different access phases

    – FOCONCSx controls the bus function of an address window

    – ADDRESELx defines and address window

C16x

- EBC

    – configuration via Register BUSCONx and SYSCON

    – ADDRESELx

XC166 architecture

C166S-V2 CPU Architecture External Bus Controller (EBC) Additional features to C16x

XC16x

- EBC

  - not required bus control signals can be used as GPIO (Port20 functionality)

C16x

- EBC

  - dedicated bus control signals: ALE, RD#, WR#

XC166 architecture