

C166S-V2 CPU Architecture

External Bus Controller (EBC)

- EBC performs all external memory accesses
- Access modes:
 - Single Chip Mode
 - 16 bit Multiplexed/Demultiplexed Mode
 - 8 bit Multiplexed/Demultiplexed Mode
- Address range:
 - 16/17/18/19.....24-bit Addresses
 - Up to 5 external CS# lines
- Access timing:
 - individual bus timing for each address window
 - 6 programmable bus cycle phases allow optimized bus access timing