

XC16x Flash

Additional features to C16x

XC16x

- Flash Process 0.22µm
- HW ECC
 - 64bit (Data) + 8bit (ECC)
- 128 byte assembly buffer
- 64bit read bus to Flash
- Flash starts at C0'0000H
- Flash programming via:
 - CPU and
 - ◆ ASC
 - ◆ JTAG

C16x

- Flash Process 0.5µm
- HW ECC
 - 32bit (Data) + 7bit (ECC)
- 64 byte assembly Buffer
- 32bit bus
- Flash starts at 00'0000H or mapped at 01'0000H
- Flash programming via:
 - CPU Host Mode
 - ◆ ASC
 - External Host Mode