Introduction

- **Reset Control Block**

  – handles the reset and startup behavior

- **Central System Control Block**

  – handles security level selection and clock control

- **Power Management Control Block**

  – supports power management, power reduction, CPU clock frequency control

- **Watchdog Timer**

  – prevent the controller from malfunctioning

  – Watchdog can only be disabled before ENIT (Compatible Mode)

  – Watchdog can be disabled/enabled after ENIT (Enhanced Mode)

XC166 architecture

Introduction

- **External Interrupt Control Block**

  – handles the 8 asynchronous fast interrupt inputs

- **Temperature Compensation Block**

  – Can reduce the output driver strength at low or medium temperature

- **Identification Control Block**

  – Six identification register (chip manufacture, chip type, etc.), can be used for automatic test selection

XC166 architecture