

Asynchronous/Synchronous Serial Interface(ASC) Overview

- Full-duplex asynchronous operating modes
 - 8- or 9-bit data frames, LSB first
 - Parity bit generation/checking
 - One or two stop bits
 - Baudrate up to 1.25 Mbaud (@ 40 MHz clock)
 - Multiprocessor mode for automatic address/data byte detection
 - Loop-back capability
 - Support for IrDA data Transmission/reception up to max. 115.2 Kbaud

- Half-duplex 8-bit synchronous operating mode
 - Baudrate up to 5 Mbaud (@ 40 MHz clock)

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- Data transmission is double buffered
- Autobaud rate detection
- Buffered transmitter/receiver with FIFO support
- Comfortable Interrupt Generation
 - on a transmitter empty condition
 - on a transmit last bit of a frame
 - on a receiver buffer full condition
 - on a error condition (receive, parity, overrun error)
 - on the start and end of a autobaud detection

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