

Capture/Compare Unit 1/2

Overview

- Four 16-bit timers with reload register
- 32 registers individually configurable for capture or compare function
- 36 interrupts: 32 capture compare interrupts with four timer interrupts
- Up to 32 software timers
- Programmable clock with multiple sources

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Overview

- 200 ns maximum resolution (@ 40 MHz staggered mode), compatible to C16x
- 25ns maximum resolution (@ 40 MHz non-staggered mode), enhanced mode
- Double register compare function
- Primary clock prescaler
- Additional output register
- Single event mode

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Overview CAPCOM 1

