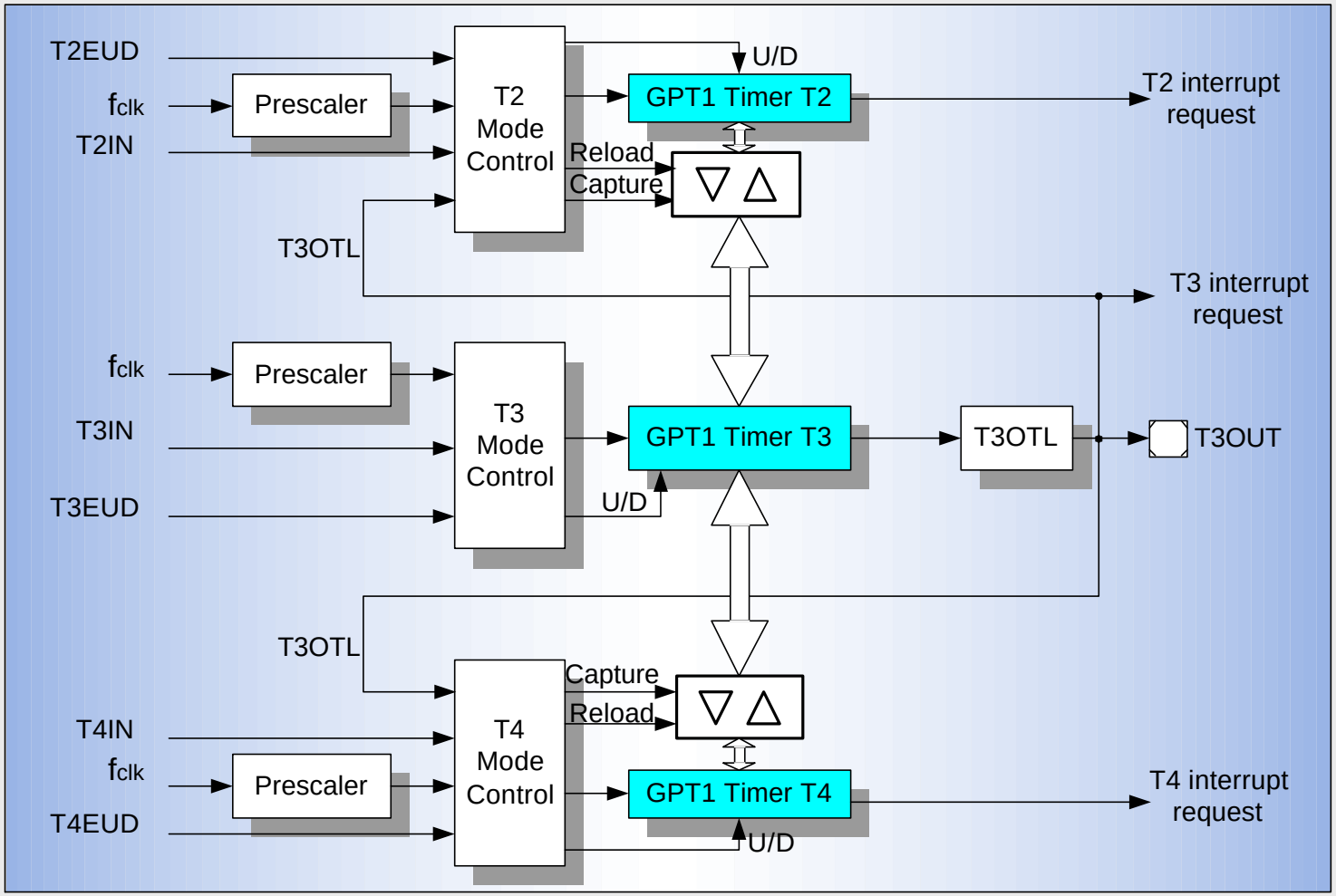
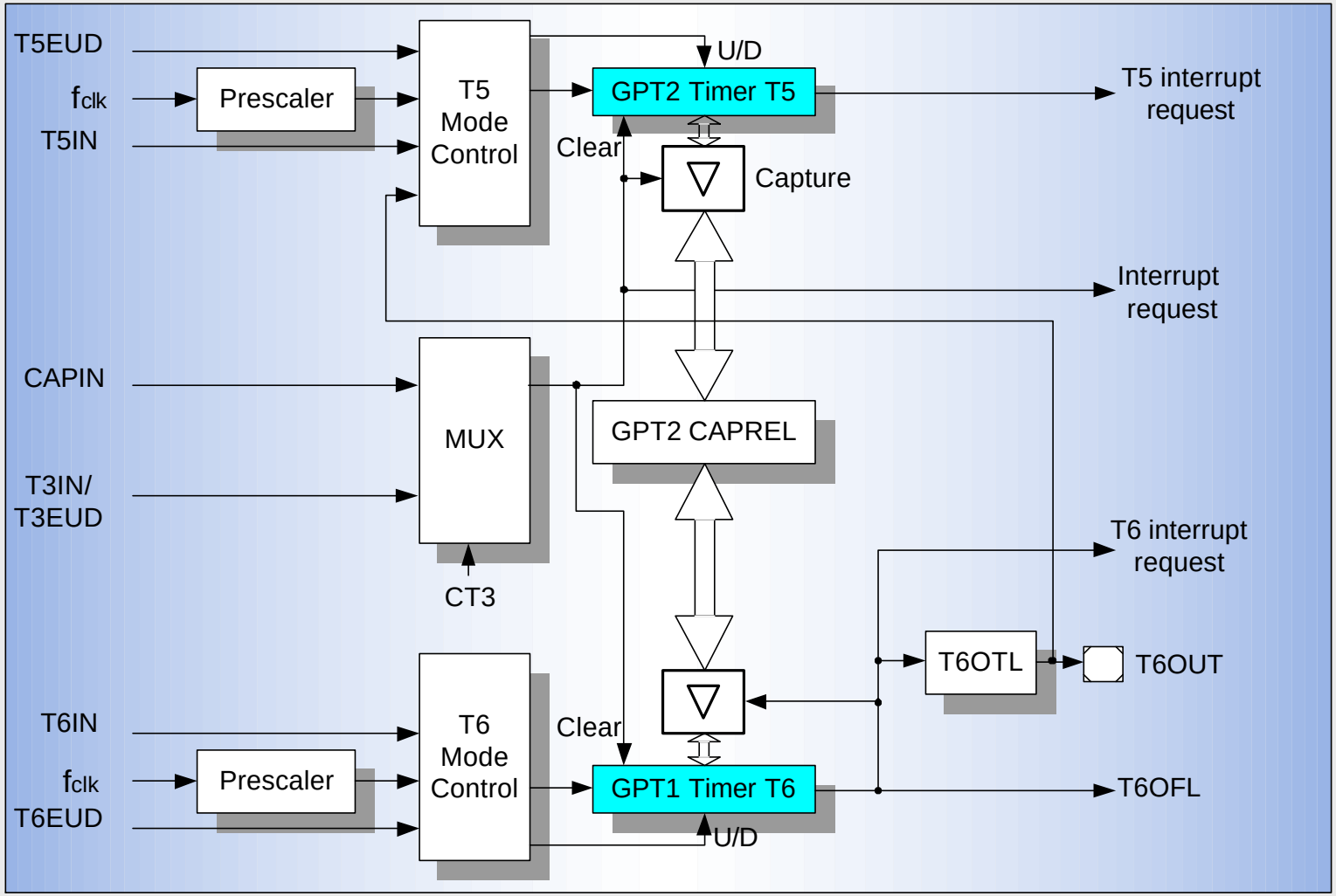
General Purpose Timer Unit (GPT12E) Overview

- Timer Block GPT1
  - f_{Clk}/4 maximum resolution (100ns @ fcpu 40 MHz)
  - clock prescaler support
  - 3 independent timers/counters (T2, T3, T4)
  - Timers/counters can be concatenated
  - 4 operating modes (timer, gated timer, counter, incremental)
  - enhanced incremental interface modes
  - separate interrupt request lines
  - auxiliary timer in reload/capture mode

General Purpose Timer Unit (GPT12E)
Timer Block 1



General Purpose Timer Unit (GPT12E) Overview

- Timer Block GPT2
  - $f_{CLK}/2$ maximum resolution (50ns @ fcpu 40 MHz)
  - clock prescaler support
  - 2 independent timers/counters (T5, T6)
  - Timers/counters can be concatenated
  - 3 operating modes (timer, gated timer, counter)
  - Extended capture/reload functions via 16-bit Capture/Reload register CAPREL
  - Separate interrupt request lines
  - auxiliary timer in reload/capture mode

General Purpose Timer Unit (GPT12E)
Timer Block 2