

High-Speed Synchronous Serial Interface (SSC) Overview

- Master and slave mode operation
 - Full-duplex or half-duplex
- Transmission / reception of data is double buffered
- Flexible
 - Programmable number of bits: 2 to 16 bit
 - Programmable shift direction: LSB or MSB shift first
 - Programmable clock polarity
 - Programmable clock/data phase
- Max. Baudrate up to 20 Mbaud (@ 40 MHz clock)

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- Comfortable Interrupt Generation
 - on a transmitter empty condition
 - on a receiver full condition
 - on a error condition (receive, phase, baudrate, transmit error)
 - Three pin interface with flexible SSC pin configuration

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